



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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7/11/03
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In re Application of

Stephen M. Gates, et al.

Serial No.: 10/080,568

Group Art Unit: 2814

Filed: February 25, 2002

Examiner: Phat X. Cao

For: FORMATION OF ARRAYS OF MICROELECTRONICS ELEMENTS

Commissioner of Patents
Alexandria, VA 22313-1450

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INFORMATION DISCLOSURE STATEMENT

Sir:

Under the provisions of 37 CFR §1.97 through §1.99 and pursuant to Applicant's duty of disclosure under 37 CFR §1.56, Applicants respectfully bring the following documents listed on the attached form PTO-1449, to the attention of the Examiner in charge of the above-identified application. Copies of the listed documents are provided herewith for the convenience of the Examiner.

This citation does not constitute an admission that the references are relevant or material to the claims. They are only cited as constituting related art of which the Applicants are aware.

It is respectfully requested that the listed references be considered by the Examiner and formally made of record in this application.

Please charge any deficiencies in fees and credit any overpayment of fees to Assignee's Deposit Account No. 50-0510.

Respectfully submitted,

Phillip E. Miller, Esq.
Registration No. 46,060

Date:

6/27/03

McGinn & Gibb, PLLC
Intellectual Property Law
8321 Old Courthouse Road, Suite 200
Vienna, VA 22182-3817
(703) 761-4100
Customer No. 21254